Notice of Allowability	Application No.	Applicant(s)	
	10/773,291	BECKERMAN, HOWARD	
	Examiner	Art Unit	
	Paul Ip	2837	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to *the application filed on 2/9/2004*.

2. ☒ The allowed claim(s) is/are *1-8*.

3. ☒ The drawings filed on *09 February 2004* are accepted by the Examiner.

4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
   a) ☐ All b) ☐ Some* c) ☐ None of the:
      1. ☐ Certified copies of the priority documents have been received.
      2. ☐ Certified copies of the priority documents have been received in Application No. _____ .
      3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

   * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
   (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
      1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
   (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date 2/9/2004
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413), Paper No./Mail Date _____ .
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

Paul Ip
Primary Examiner
AU 2837

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Howard Beckerman on April 25, 2005.

The application has been amended as follows:

1. (Amended) A motorized door/gate operator the improvement comprising[;];

[An Open logical "AND" arrangement] a logical means connected such that at least one input produces an open-output-signal and all other inputs disables the open-output-signal[.];

[a close logical "AND" arrangement] a logical means connected such that at least one input produces a close-output-signal and all other inputs disables the close-output-signal[.];

[stated] said open-output-signal connects to inverting means that disables the [aforementioned] close logical ["AND"] means, thereby disabling the close-output-signal[.];

[An] an open-to-close delay circuit, arranged such that it delays [stated] the close-output-signal only after receiving the [stated] open-output-signal[.],

[Otherwise] otherwise, no significant open-output-signal delay is present[.];

[A] a close-to-open delay circuit, arranged such that it delays [stated] the open-output-signal only after receiving [a] the close-output-signal[. Otherwise], otherwise, no significant open-output-signal delay is present[.];

[Switching] a first switching means that reacts to [stated] the open-to-close delay output signal to supply power line voltage to [the] a motor causing it to rotate in one direction[.]; and

[Switching] a second switching means that reacts to [stated] the close-to-open delay output signal to apply power line voltage to the motor causing it to rotate in the opposite direction.

2. (Amended) A motorized door/gate operator the improvement comprising[;]:

[A] a close limit of travel sensing means connecting to change the logical operation of obstruction sensing from opening [the] a motor if obstructed to stopping the motor if obstructed[.];

[The] the close limit of travel sensing means also connects to a delay circuit, arranged such that it delays the close limit signal forming a [new signal called the] virtual-close-limit signal[.];

the [Virtual-close-limit] virtual-close-limit signal connects to stop rotation of the motor in the close direction[.]; and

[An] an open limit sensing means connects to stop rotation of the motor in the open direction.

3. (Amended) The [means] motorized door/gate operator according to [claim-2] claim 1 further comprising:

[Stated] the open limit and the [stated] virtual-close-limit signal connect into a logical ["OR"] means producing a new either-limit-signal[.];

[Stated] the either-limit-signal couples to a one-shot circuit, producing one short duration pulse each time it is activated[.]; and

[Stated] the short duration pulse connects to stop the motor operator whenever the either-limit-signal activates.

4. (Amended) The [means] motorized door/gate operator according to [claim-2] claim 2 further comprising:

[Switching] switching means to reverse the limit sensing signals such that [stated] the close limit of travel becomes the [stated] open limit of travel and conversely the open limit becomes the close limit.

5. (Amended) The [means] motorized door/gate operator according to [claim-4] claim 4 further comprising:

[Lights'] light means indicating which particular limit sensor is active[.];

[Placing] placing [such lights] the light means next to said limit-sensors such that, when the motor produces the correct rotation [the] a moving mechanical position indicator moves toward the illuminated light means[.]; and

[Conversely] conversely, when the motor produces the incorrect rotation the moving mechanical position indicator moves away from the illuminated light.

6. (Amended) [A] The motorized door/gate operator according to claim 2 further [the improvement] comprising:

[Relay] first relay means to rotate an electric [motors] motor shaft in one direction and a [Second] second relay means to rotate the motor shaft in an opposite direction[.];

[A] a first signal represents an opening command and a second signal represents a closing command[.];

[A] a switch selects one of two rotational directions[.];

[Means] means configures to reverse [stated] the first signal and the second signal in response to the position of the [stated] switch[.]; and

[Means] means to energize the relays based on reversing the first and second signals.

7. (Amended) [A] The motorized door/gate operator [the improvement] according to claim 1 further comprising:

[An] an open switch signal and a close switch signal, connects to [Logical "OR" arrangement] a first logical means [thereby] producing at its output an [first] either-switch-signal[.];

[Signal] signal indicating that a low voltage exists and signal of the activation of a stop pushbutton switch connecting to [another] a second logical ["OR"] means to produce an [second] All-Stop Signal[.], [Such] such All-stop signal connects to stop the opening and closing of the motor operator[.];

[Stated] the [first] either-switch signal and [stated] the [second] All-Stop signal connects to a logical means ["AND"] [function] producing at its output a third signal[.], [Such] such [the] third output signal indicates pressing either pushbutton

at the same time as a low voltage is present, or while a stop pushbutton is pressed[.]; and

[Feeding] feeding back [stated] the third signal into the logical ["OR"] means producing [stated] an [second] All-Stop signal thereby latching the [second] All-Stop signal until removal of the [first] [either switch signal] either-switch-signal.

8. (Amended) [A] The motorized door/gate operator [the improvement] according to claim 1 further comprising:

[A] a close pushbutton switch connects such as to produce a [first] close-switch-signal[.];

[Stated] the close-switch-signal couples to a one-shot-circuit, producing a short duration pulse with each press of the close pushbutton switch[.];

[The] the short duration pulse connects to stop the opening cycle of the motor operator[.]; and

[Stated] the [first] close-switch-signal also connects to start rotation of the motor in the close direction.

Cancel claim 9.

EXAMINER'S REASONS FOR ALLOWANCE

2. The following is an examiner's statement of reasons for allowance:

Claims 1-8 have been carefully considered in view of the specification of the invention. Claims 1-8 are presented in the form of means plus function(s). Accordingly,

the claims have been interpreted in view of the specification with respect to figure 1 of the invention under 35 U.S.C. 112, sixth paragraph. .

Claim 1 recites a motorized door/gate operator improvement comprising an open logical AND gate and a close logical AND gate connected to produce an open-output-signal and a close-output-signal respectively. The open-output-signal connects to inverting means that disables the close logical AND thereby disabling the close-output-signal. An open-to-close delay circuit is arranged such that it delays the close-output-signal only after receiving the open-output-signal. Otherwise, no significant close-output-signal delay is present. A close-to-open delay circuit is arranged such that it delays the open-output-signal only after receiving a close-output-signal. Otherwise, no significant open-output-signal delay is present. A first switching means reacts to the open-to-close delay output signal to apply power line voltage to the motor causing it to rotate in one direction. A second switching means reacts to the close-to-open delay output signal to apply power line voltage to the motor causing it to rotate in the opposite direction.

Claim 2 recites a motorized door/gate operator improvement comprising a close limit of travel sensing means connecting to change the logical operation of obstruction sensing from opening a motor if obstructed to stopping the motor if obstructed. The close limit of travel sensing means also connects to a delay circuit, arranged such that it delays the close limit signal forming a virtual-close-limit signal. The virtual-close-limit signal connects to stop rotation of the motor in the close direction. An open limit sensing means connects to stop rotation of the motor in the open direction.

The references of the record neither taken alone nor in combination fail to teach or suggest the circuit configuration as recited in the claims of the invention. The references of the record also fail to teach or suggest the means plus function(s) as recited in the claims of the invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

COMMUNICATION INFORMATION

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul Ip whose telephone number is (571)-272-1941. The examiner can normally be reached on Monday to Friday from 6:30 am to 3:00 pm Eastern time.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Martin, can be reached on (571)-272-2107. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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4/27/05